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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/782,151

02/14/2001

Ralph E. Frazier

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05/31/2006

PAUL W. MARTIN
NCR CORPORATION, LAW DEPT.
1700 S. PATTERSON BLVD.
DAYTON, OH 45479-0001

EXAMINER

YIGDALL, MICHAEL J

ART UNIT

PAPER NUMBER

2192

DATE MAILED: 05/31/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/782,151

Applicant(s)

FRAZIER, RALPH E.

Examiner

Michael J. Yigdall

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 March 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. This Office action is responsive to Applicant's submission filed on March 8, 2006.

Claims 1-20 are pending.

Response to Amendment

2. The rejection of claims 2 and 18-20 under 35 U.S.C. 103(a) as being unpatentable over Bishop in view of Barritz in view of Farrell in view of Yamagishi is withdrawn in view of Applicant's amendment.

Response to Arguments

3. Applicant's arguments have been fully considered but they are not persuasive.

Applicant hints at an allegation of improper hindsight reconstruction (remarks, page 7, last paragraph to page 8, top).

In response, it must be recognized that any judgment on obviousness is in a sense necessarily a reconstruction based upon hindsight reasoning. But so long as it takes into account only knowledge which was within the level of ordinary skill at the time the claimed invention was made, and does not include knowledge gleaned only from the applicant's disclosure, such a reconstruction is proper. See *In re McLaughlin*, 443 F.2d 1392, 170 USPQ 209 (CCPA 1971).

Moreover, it should be noted that reliance on a large number of references in a rejection does not, without more, weigh against the obviousness of the claimed invention. See *In re Gorman*, 933 F.2d 982, 18 USPQ2d 1885 (Fed. Cir. 1991).

Applicant's arguments against the individual teachings of each reference (remarks, page 8, last paragraph to page 12, last paragraph) were addressed in the final Office action mailed on April 19, 2005 and are still found not persuasive.

Specifically, Applicant's arguments against each reference are directed to features for which the reference is not expressly relied upon.

Moreover, it should be noted that one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981), and *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1 and 3-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6,049,798 to Bishop et al. (art of record, "Bishop"), in view of U.S. Patent No. 5,590,056 to Barritz (art of record, "Barritz"), in view of U.S. Patent No. 5,247,675 to Farrell et al. (art of record, "Farrell"), in view of U.S. Patent No. 5,870,604 to Yamagishi (art of record, "Yamagishi").

With respect to claim 1 (currently amended), Bishop discloses a method of capturing operating software scheduling information during execution of operating software (see, for example, the abstract, which shows capturing internal resource information such as CPU and memory availability or utilization, i.e. scheduling information, in real-time, i.e. during execution of the operating software) and recording said operating software scheduling information from within the operating software (see, for example, column 11, lines 1-10, which shows that the scheduling information is recorded from within the operating software), the method comprising the steps of:

(a) compiling operating software scheduling information capture software which is an integral part of the operating system (see, for example, column 11, lines 1-4, which shows a service of the operating system, inherently compiled, that is used for capturing event traces of process activity, i.e. scheduling information).

Although Bishop discloses recording the data for a certain amount of time in order to provide past records, i.e. a history of events (see, for example, column 22, lines 18-22), and discloses identifying each task by process ID and process name (see, for example, FIG. 13A), Bishop does not expressly disclose the limitation wherein the operating software scheduling information capture software records a history of the operating software events as they occur, information related to the history being organized and stored as operating software program scheduling information relating to interactions between the operating system software and each of the programs and tasks managed by the operating system software.

However, Barritz discloses monitoring events as they occur and recording an event history log (see, for example, FIG. 5 and column 6, lines 54-57), in which the recorded

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information comprises job scheduling information for each module or task managed by the scheduler, i.e. the operating system software (see, for example, column 6, lines 58-64), so as to identify software usage patterns (see, for example, column 2, line 63 to column 3, line 4).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to supplement the capture system of Bishop with the event history features taught by Barritz, for the purpose of identifying software usage patterns.

Although Bishop discloses monitoring and capturing the information to improve the efficiency of a computer system (see, for example, column 3, lines 12-33), Bishop does not expressly disclose the limitation wherein the scheduling information includes indications of relative priorities of programs and tasks.

However, Farrell discloses scheduling information that indicates the relative priorities of threads or tasks (see, for example, thread state descriptor 19 in FIG. 2, and column 4, line 62 to column 5, line 12), which is used to optimize program execution (see, for example, column 2, lines 19-24).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to supplement the information captured by Bishop with the relative priorities of programs and tasks, such as taught by Farrell, in order to further improve execution efficiency.

Bishop further discloses the limitation wherein the scheduling information includes indications of transfers of control from lower priority to higher priority tasks (see, for example, FIG. 13A, which shows trace records for process switches, i.e. transfers of control among tasks, and column 10, lines 62-67, which shows that the tasks have lower and higher priority levels).

Although Bishop discloses monitoring and capturing the information to improve the efficiency of a computer system (see, for example, column 3, lines 12-33), Bishop does not expressly disclose the limitation wherein the scheduling information includes identification of tasks waiting for execution at the occurrence of each software event.

However, Yamagishi discloses scheduling information that identifies the number of jobs or tasks waiting for execution (see, for example, operation status table 9 and heading 92 in FIG. 3). A CPU monitor captures the information from a job scheduler (see, for example, column 3, lines 59-65) to distribute the load among processors, thereby improving efficiency (see, for example, column 2, lines 36-39).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to supplement the information captured by Bishop with the tasks waiting for execution, such as taught by Yamagishi, in order to further improve execution efficiency.

Bishop further discloses the steps of:

(b) invoking operating software scheduling information capture (see, for example, column 20, line 55 to column 21, line 4, which shows a procedure for invoking the capture of the data); and

(c) recording operating software scheduling information while the operating software is executing, for relatively long duration storage in order to permit review of the scheduling information by a user (see, for example, column 3, lines 12-33, which shows recording the scheduling information in real time, i.e. while the operating software is executing, and column 22, lines 18-22, which further shows recording the data for a relatively long duration for review of past records by a user).

Barritz further discloses the limitation wherein the duration of storage is sufficient to allow data collected during an operating session to be retrieved and used after termination of the session (see, for example, column 10, lines 28-44, which shows that the information is recorded to a log for storage, and column 8, lines 43-63, which shows that the information is later retrieved from the log after one or more operating sessions to produce statistical reports).

With respect to claim 3 (original), Bishop further discloses the limitation wherein the operating software scheduling information recorded includes information updated or maintained by the operating software in relation to the scheduling of a program (see, for example, FIG. 13A, which shows trace records that include execution time and interrupt time, information maintained by the operating system in relation to scheduling).

With respect to claim 4 (original), Bishop in view of Barritz, Farrell and Yamagishi further discloses the limitation wherein the operating software scheduling information recorded includes task identification, task priority, and task run-time length (see, for example, Bishop, FIG. 13A, which shows trace records that include the process ID, i.e. task identification, and the execution time, i.e. run-time length; also see, for example, Farrell, FIG. 2, which shows a thread state descriptor that includes the thread or task priority).

With respect to claim 5 (original), Bishop in view of Barritz, Farrell and Yamagishi further discloses the limitation wherein the operating software scheduling information includes a task waiting count (see, for example, Yamagishi, FIG. 3, which shows an operation status table that includes the number of jobs waiting for execution, i.e. a task waiting count).

With respect to claim 6 (original), Bishop further discloses the limitation wherein the operating software scheduling information is recorded to a ledger (see, for example, column 20, lines 36-37 and 50-52, which shows that the data is initially recorded to a buffer or ledger).

With respect to claim 7 (original), Bishop further discloses the limitation wherein the ledger is at least one of a circular or fixed length ledger (see, for example, column 21, lines 23-26, which shows that data is discarded from the pipe if it is not read quickly enough, i.e. because the buffer is a circular ledger having a fixed length).

With respect to claim 8 (original), Bishop further discloses the limitation wherein the scheduling information includes at least one of the number of program schedules, program preempts, and interrupts (see, for example, column 14, lines 14-15 and 35-45, which shows that interrupts are recorded).

With respect to claim 9 (original), Bishop further discloses the limitation wherein the scheduling information includes at least one of the highest priority attained, program identity, and length of run-time (see, for example, column 21, lines 49-52, which shows that the process name, i.e. the program identity, is included in the captured scheduling information).

With respect to claim 10 (original), Bishop further discloses the limitation wherein the scheduling information includes at least one of the lowest priority attained, program identity, and length of run-time (see, for example, column 21, lines 49-52, which shows that the process name, i.e. the program identity, is included in the captured scheduling information).

With respect to claim 11 (original), Bishop further discloses the limitation wherein the scheduling information includes at least one of the number of times in the idle loop and length of run-time (see, for example, FIG. 13A, which shows trace records that include the execution time, i.e. the length of run-time).

With respect to claim 12 (original), Bishop further discloses the limitation wherein the scheduling information includes a sequential record of at least one of scheduled programs, priorities, and events (see, for example, column 15, lines 54-64, which shows that events are matched with timing information to compose a sequential record of events).

With respect to claim 13 (original), Bishop in view of Barritz, Farrell and Yamagishi further discloses the limitation wherein the scheduling information includes at least one of the number and identity of programs waiting to run (see, for example, Yamagishi, FIG. 3, which shows an operation status table that includes the number of jobs waiting to run).

With respect to claim 14 (original), Bishop further discloses the limitation wherein the operating software scheduling information capture is invoked on an event occurrence (see, for example, column 14, lines 14-15 and 55-60, which shows that events such as a mode switch can invoke the capture of scheduling information).

With respect to claim 15 (currently amended), the limitations recited in the claim are analogous to those of claim 1 (see the rejection of claim 1 above).

With respect to claim 16 (original), Bishop further discloses the limitation wherein said operating software scheduling information capture software is not resident on an external device

(see, for example, column 4, lines 32-34, which shows that the software can be internal to the system, and column 22, lines 59-63, which shows that external hardware is not needed).

With respect to claim 17 (original), Bishop further discloses the limitation wherein said operating software scheduling information capture software is not a separate task scheduled by an operating software scheduler (see, for example, column 11, lines 1-10 and 30-35, which shows that the information capture is performed by a device driver, i.e. not by a separate task scheduled by a operating software scheduler).

6. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bishop in view of Barritz in view of Farrell in view of Yamagishi, as applied to claim 1 above, and further in view of U.S. Patent No. 6,795,809 to O'Brien et al. (now made of record, "O'Brien").

With respect to claim 2 (currently amended), Bishop in view of Barritz, Farrell and Yamagishi further discloses selecting the information to be recorded by the operating software scheduling information capture software (see, for example, Barritz, column 10, lines 45-49), but Bishop in view of Barritz, Farrell and Yamagishi does not expressly disclose:

- (a) scanning a specific bar code tag sequence with a retail bar code scanner; and
- (b) selecting the information to be recorded by the operating software scheduling information capture software based upon the specific bar code tag sequence.

However, O'Brien discloses scanning a specific bar code tag sequence with a retail bar code scanner (see, for example, column 2, lines 27-35) and selecting information to be recorded based upon the specific bar code tag sequence (see, for example, column 12, lines 40-53).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to supplement the system of Bishop, Barritz, Farrell and Yamagishi with the bar code scanning features of O'Brien. Incorporating a retail store bar code scanner into the system of Bishop, Barritz, Farrell and Yamagishi in such a manner would enable the selection of information to be recorded by the operating software scheduling information capture software without manual data entry, saving time and effort and reducing errors.

7. Claims 18-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bishop in view of Barritz in view of Farrell in view of Yamagishi in view of O'Brien.

With respect to claim 18 (currently amended), the limitations recited in the claim are analogous to those of claim 2 (see the rejections of claims 1 and 2 above). Bishop further discloses a processor for receiving and transmitting data (see, for example, CPU 190 in FIG. 14) and a memory coupled to the processor (see, for example, RAM 193 in FIG. 14), the memory storing instructions to be executed by the processor.

With respect to claim 19 (original), Bishop further discloses the limitation wherein said operating software scheduling information capture software is internally processed on said processor (see, for example, column 4, lines 32-34, which shows that the software can be internal to the system).

With respect to claim 20 (original), Bishop further discloses the limitation wherein said operating software scheduling information capture software is not a separate task scheduled by an operating software scheduler (see, for example, column 11, lines 1-10 and 30-35, which

shows that the information capture is performed by a device driver, i.e. not by a separate task scheduled by an operating software scheduler).

Conclusion

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael J. Yigdall whose telephone number is (571) 272-3707. The examiner can normally be reached on Monday through Friday from 7:30am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam can be reached on (571) 272-3695. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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Michael J. Yigdall
Examiner
Art Unit 2192

mjy



TUAN DAM
SUPERVISORY PATENT EXAMINER